



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 7356

SERIAL NUMBER 10/815,083	FILING DATE 03/31/2004 RULE	CLASS 257 438	GROUP ART UNIT 2891	ATTORNEY DOCKET NO. 97-1363.5
-----------------------------	---------------------------------------	---------------------	------------------------	-------------------------------------

APPLICANTS

Salman Akram, Boise, ID;

Warren M. Farnworth, Nampa, ID;
Alan G. Wood, Boise, ID;

** CONTINUING DATA *****

This application is a CON of 09/961,646 09/25/2001 PAT 6,833,613
 which is a DIV of 09/385,606 08/30/1999 PAT 6,294,837
 which is a DIV of 08/993,965 12/18/1997 PAT 6,107,109

LT

** FOREIGN APPLICATIONS *****

(None) LT

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 06/08/2004

Foreign Priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY ID	SHEETS DRAWING 6	TOTAL CLAIMS 31	INDEPENDENT CLAIMS 3
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance Examiner's Signature — Initials				

ADDRESS

22823
 STEPHEN A GRATTON
 THE LAW OFFICE OF STEVE GRATTON
 2764 SOUTH BRAUN WAY
 LAKWOOD , CO
 80228

TITLE

Method for fabricating semiconductor components by forming conductive members using solder

FILING FEE	FEES: Authority has been given in Paper	<input type="checkbox"/> All Fees
		<input type="checkbox"/> 1.16 Fees (Filing)
		<input type="checkbox"/> 1.17 Fees (Processing Ext. of